IEEE

NEWCAS 2027

IEEE 25th Interregional NEWCAS Conference

Cork, Ireland

20-23 June 2027



AN IEEE CAS SOCIETY INTERREGIONAL FLAGSHIP CONFERENCE

CALL FOR PAPERS

IMPORTANT DATES

December 1, 2026
Paper submission website opens

December 8, 2026
Special sessions proposals deadline

February 1, 2027
Paper submission deadline

February 1, 2027
Tutorial proposal submission deadline

February 1, 2027
Special sessions paper submission deadline
(papers not for publication can be submitted later)

March 1, 2027
Registration opens

April 9, 2027
Decision Notification

April 30, 2027
Final Paper submissions, Early Registration Deadline

ORGANIZING COMMITTEE

General Co-Chairs

Daniel O'Hare
Tyndall National Institute

Barry Cardiff
University College Dublin

TPC Co-Chairs

Elena Blokhina
University College Dublin

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Memorial University of Newfoundland

Special Session Co-Chairs

Deepu John
University College Dublin

Giulia Di Capua
University of Cassino and Southern Lazio

Sylvain Bourdel
University of Grenoble

Tutorial Co-Chairs

Gerardo Salgado
Tyndall National Institute

Barry Cardiff
University College Dublin

THEME: AI ASSISTED DESIGN OF CIRCUITS AND SYSTEMS

The 25th IEEE Interregional NEWCAS Conference will be held in Cork, Ireland, on June 20-23, 2027. The program of the conference will be tailored to reflect the wide spectrum of topics and research interests shared among the CAS community. The conference aims to attract papers from both academic and industrial research. In keeping with the conference theme there will be invited papers on new AI assisted methods to design RF, analog and digital circuits and systems.

Papers can be submitted to the following tracks:

- Analog & Mixed-Signal Circuits, Sensory Systems
- Data Converters
- Digital & Embedded Circuits & Systems
- Biomedical & Wearable Systems
- Wireless & Wireline Communication Circuits & Systems
- RF, Microwave, mmWave & THz Circuits
- Power Management, Energy Harvesting & Power Electronics
- Photonic & Optoelectronics Circuits & Systems
- EDA, CAD & Design Tools
- Neuromorphic & Neural Inspired Circuits & Systems
- Circuits & Systems for AI
- Hardware security & cryptography
- Quantum Computing & Cryo-CMOS
- Emerging Technologies & Technology Trends
- Special Sessions

SUBMISSIONS

The organizing committee invites prospective authors to submit complete 4-page papers in standard IEEE double-column format, with the possibility of an additional page dedicated to references, to clearly present the work, methods, results, originality, significance, superiority and applications of the techniques discussed. The papers must be submitted in electronic PDF format.

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CAS IEEE CIRCUITS AND SYSTEMS SOCIETY

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